

Texas Instruments Incorporated

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FAX COVER SHEET

DATE: 11/9/04

Page 1 of 5

FROM: ROBERT MARSHALL	TO: EXAM. DAVE HULISMAN
COMPANY NAME: Robin Barnum Texas Instruments Incorporated	COMPANY NAME:
PHONE NUMBER: (972) 917-5296 // 917-4374	PHONE NUMBER: 571-272-4168
FAX NUMBER: (972) 917-4418	FAX NUMBER: 703-872-9306

MESSAGE

TI-30559

S/N 09/702,320

DAVE, Per our phone conversation earlier today, attached are copies of the RCE, Fax PC, TI Internal Fax Acknow., and PTO Auto Reply Fax Acknow, since you never received the first sent RCE.

Robin Barnum
for Robert D. Marshall Jr.

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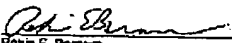
Auto-Reply Facsimile Transmission



TO: Fax Sender at 972 917 4418
Fax Information
Date Received: 5/3/2004 10:45:45 AM [Eastern Daylight Time]
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NOV-03-2004 09:39 FACD6133		972 917 4418 P.01	
To:	Technology Center: 2183 Facsimile Number: 703-872-9306	Total Pages Sent: 2	
From:	Robert D. Marshall, Jr. Texas Instruments Incorporated Facsimile: 972-917-4418 Phone: 972-917-5290		
IN THE UNITED STATES PATENT AND TRADEMARK OFFICE			
In re the Application of	Laurence R. Sinar, Jr., et al.	TI-30559	Art Unit: 2183
Serial No.:	09/702,320	Examiner:	David J. Huisman
Filed:	October 31, 2000	Conf. No.:	0784
For:	Microprocessor With Execution Packet Spanning Two or More Fetch Packets		
CERTIFICATION OF FACSIMILE TRANSMISSION			
I hereby certify that the following papers are being transmitted by facsimile to the U.S. Patent and Trademark Office at 703-872-9306 on the date shown below:			
 Robin E. Barnum		Date: May 3, 2004	
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NAME OF APPLICANT Laurence R. Sinar, Jr., et al.		Serial No.: 09/702,320	
TITLE OF INVENTION Microprocessor With Execution Packet Spanning Two or More Fetch Packets		Filing Date: October 31, 2000	
INVENTOR TI-30559 20-0468		ATTORNEY SINAR 20-0468	
<small>This form is to be used only for the use of the address named and contains legally privileged and/or confidential information. If you are not the authorized recipient of this message, you are hereby notified that any disclosure, distribution, copying or use of this communication is strictly prohibited. Applicable privileges are not waived by virtue of the document having been transmitted by facsimile. Any funds received in connection with this message should be returned to the sender by mail at the address indicated on this cover sheet.</small>			
<small>Texas Instruments Incorporated PO Box 55414, M/S 969 Dallas, TX 75255</small>			
PAGE 10 * RCVD AT 5/3/2004 11:55:13 AM * EPOCH 2.0 * FAX 972 917 4418 * DURATION (mm:ss): 02:12			

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STN NO.	COMM.	ONE-TOUCH/ ABBR NO.	STATION NAME/EMAIL ADDRESS/TELEPHONE NO.	PAGES	DURATION
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To: Technology Center: 2183
Facsimile Number: 703-872-9306

Total Pages Sent: 2

From: Robert D. Marshall, Jr.
Texas Instruments Incorporated
Facsimile: 972-917-4418
Phone: 972-917-5290

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

TI-30559

Laurence R. Simar, Jr., et al.

Art Unit: 2183

Serial No.: 09/702,320

Examiner: David J. Huisman

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Conf. No.: 9784

For: Microprocessor With Execution Packet Spanning Two or More Fetch Packets

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Robin E. Bamum

May 3, 2004
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FACSIMILE COVER SHEET

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<input type="checkbox"/> CONTINUATION APP'N (# Pages)	<input checked="" type="checkbox"/> Request for Continued Examination (RCE) (1 page)
<input type="checkbox"/> DIVISIONAL APP'N	
NAME OF INVENTOR(S): Laurence R. Simar, Jr., et al.	
TITLE OF INVENTION: Microprocessor With Execution Packet Spanning Two or More Fetch Packets	
TI FILE NO: TI-30559	DEPOSIT ACCE. NO: 28-0668
FAXED: 5/3/04	
DUE: 5/19/04	
ATTY/SECY: RDM/reb	
RECEIPT DATE & SERIAL NO: Serial No.: 09/702,320	
Filing Date: October 31, 2000	

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